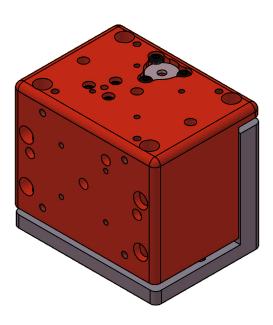




ALIO STAGE AND MOTOR SPECIFICATIONS

MODEL	UNITS	AI-VC-600-Z-SCB
NOMINAL TRAVEL FROM HOME INDEX	mm	+/- 3
MAGNETIC LIMIT LOCATIONS (+0.25/-2.5mm)	mm	+/- 3
HARD STOP LOCATIONS (+/- 0.25mm)	mm	+/- 3.6
PERFORMANCE SPECIFICATIONS [1]		(STD) ULTRA NANO
HOME INDEX BIDIRECTIONAL REPEATABILITY		< +/- 1 encoder count
LINEAR DISPLACEMENT ACCURACY	um	CONTACT ALIO INDUSTRIES
BIDIRECTIONAL LINEAR REPEATABILITY	nanometers	
STRAIGHTNESS IN X	um	
STRAIGHTNESS IN Y [2]	um	
PITCH ABOUT X [2]	arc-sec	
PITCH ABOUT Y	arc-sec	
PITCH ABOUT Z	arc-sec	
MOTION PROFILE SPECIFICATIONS		
MAX VELOCITY [3]	m/s	0.3
MAX PEAK ACCELERATION [3]	G	4.0
MAX PAYLOAD CAPABILITY	kg	2.5
ASSEMBLY MASS	kg	0.9
MOVING MASS	kg	0.2
MOTOR INFORMATION		
MOTOR TYPE		VOICE COIL
MOTOR MODEL		AI-VC-144AAA
MAX VOLTAGE (LINE TO LINE) [4]	V	17.0
ELECTRICAL TIME CONSTANT (@ 25°C)	msec	0.304
MAX MOTOR TEMP	°C	155
MOTOR THERMISTOR		NONE
MOTOR CONSTANT	N/sqrt(W)	4.386
FORCE CONSTANT	N/Apk	7.34
PHASE RESISTANCE (@ 25°C) [5]	Ohm	2.8
INDUCTANCE @ 1kHz	mH	0.850
CONTINUOUS FORCE [6] [8]	N	15.57
CONTINUOUS CURRENT [6]	Apk	2.1
PEAK FORCE [7] [8]	N	44.48
PEAK CURRENT [7]	Apk	6.06
BACK EMF CONSTANT	V/m/s	7.35

- 1. Specifications measured on stage centerline at nominal 20°C, 50mm above mounting surface with no payload. For up to 150mm cantilever payloads.
- 2. Flatness and Pitch specifications dependent on system base. Contact ALIO for more informatic
- 3. Stage limitation at no load. Based on 100% S-curve. Does not account for limitations due to am
- 4. Back EMF plus IR drop must not exceed maximum line to line bus voltage.
- 5. Resistance values do not include cable resistance. Cable resistance adds 0.117 ohm/m for vo
- 6. Continuous operating limits are based on continuous operation at maximum temperature with a
- 7. Maximum on time at peak operating limits is 10 seconds.
- 8. Voice Coil (VC only) forces are reduced by 25% at ends of travel.
- 9. All electrical specifications may vary by 12% from listed values.
- 10. Additional motor and travel options are available for each stage for optimized performance as



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